



IPW

XA-9721A
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Keisuke TSUKAMOTO et al.

Appln. No.: 10/786,334

Group Art Unit: 2811

Filed: February 26, 2004

Examiner: C. Nguyen

For: A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND A METHOD
OF MANUFACTURING THE SAME

* * *

RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responding to the Office Action mailed December 28, 2004, and to the requirement for restriction therein, Applicants elect the invention Group II (Claims 32-41) for examination, reserving the right to file one or more divisional applications with regard to the non-elected invention.

An action on the merits of the claims to the elected invention is respectfully requested.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees under 37 C.F.R. §§1.16 and 1.17 which may be required by this paper, and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and

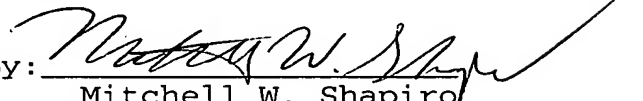
has not been requested separately, then such extension is
hereby requested.

MWS:jab

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January 28, 2005

Respectfully submitted,

By: 
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